



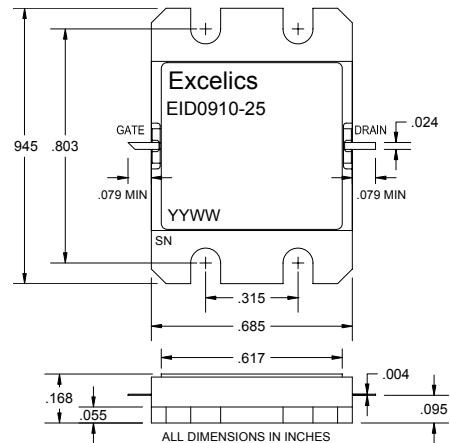
EID0910-25

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9.50-10.50 GHz 25-Watt Internally Matched Power FET

FEATURES

- 9.50– 10.50GHz Bandwidth
- Input/Output Impedance Matched to 50 Ohms
- +44 dBm Output Power at 1dB Compression
- 8.0 dB Power Gain at 1dB Compression
- 26% Power Added Efficiency
- -38 dBc IM3 at Po = 30.0 dBm SCL
- Hermetic Metal Flange Package
- 100% Tested for DC, RF, and R_{TH}



ELECTRICAL CHARACTERISTICS (T_a = 25°C)



Caution! ESD sensitive device.

SYMBOL	PARAMETERS/TEST CONDITIONS ¹	MIN	TYP	MAX	UNITS
P_{1dB}	Output Power at 1dB Compression f = 9.50-10.50GHz V _{DS} = 10 V, I _{DSQ} ≈ 4000mA	43	44		dBm
G_{1dB}	Gain at 1dB Compression f = 9.50-10.50GHz V _{DS} = 10 V, I _{DSQ} ≈ 4000mA	7.0	8.0		dB
ΔG	Gain Flatness f = 9.50-10.50GHz V _{DS} = 10 V, I _{DSQ} ≈ 4000mA			±0.6	dB
PAE	Power Added Efficiency at 1dB Compression V _{DS} = 10 V, I _{DSQ} ≈ 4000mA f = 9.50-10.50GHz		26		%
I_{d1dB}	Drain Current at 1dB Compression f = 9.50-10.50GHz		8000	9000	mA
IM3	Output 3rd Order Intermodulation Distortion Δf = 10 MHz 2-Tone Test; Pout = 30.0 dBm S.C.L. ² V _{DS} = 10 V, I _{DSQ} ≈ 65% IDSS f = 10.50GHz	-33	-38		dBc
I_{DSS}	Saturated Drain Current V _{DS} = 3 V, V _{GS} = 0 V		14000	18000	mA
V_P	Pinch-off Voltage V _{DS} = 3 V, I _{DS} = 140 mA		-1.2	-2.5	V
R_{TH}	Thermal Resistance ²		1.4	1.6	°C/W

Note: 1) Tested with 25 Ohm gate resistor. 2) Overall Rth depends on case mounting.

ABSOLUTE MAXIMUM RATING^{1,2}

SYMBOL	CHARACTERISTIC	VALUE
V _{DS}	Drain to Source Voltage	10 V
V _{GS}	Gate to Source Voltage	-3.0 V
I _{DS}	Drain Current	IDSS
I _{GSF}	Forward Gate Current	500 mA
P _{IN}	Input Power	@ 3dB compression
P _T	Total Power Dissipation	94 W
T _{CH}	Channel Temperature	175°C
T _{STG}	Storage Temperature	-65/+175°C

Notes: 1. Exceeding any of the above ratings may result in permanent damage.
2. Exceeding any of the above ratings may reduce MTTF below design goals.

Specifications are subject to change without notice.

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